

# Survey of Reconfigurable Data Center Networks: Enablers, Algorithms, Complexity

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## Abstract

Emerging optical technologies introduce opportunities to reconfigure network topologies at runtime. The resulting topological flexibilities can be exploited to design novel demand-aware and self-adjusting networks. This paper provides an overview of the algorithmic problems introduced by this technology, and surveys first solutions.

## 1 Introduction

Communication networks have become a critical infrastructure of our digital society. This introduces increasingly stringent requirements on the dependability and performance of such networks. At the same time, network traffic is growing explosively, a trend which is likely to continue [71]: next-generation workloads, such as (distributed) machine learning and artificial intelligence will lead to additional workloads headed for the world's data centers. Indeed, network traffic is growing particularly fast in data centers [83], also because many applications generate much internal data center traffic: the traffic staying inside the data center is often much larger than the traffic entering or leaving the data center [71].

Motivated by these trends, the networking community has recently created massive efforts to the design more efficient data center architectures, developing new scheduling [4], load-balancing [40], monitoring [68], and congestion control [27] algorithms, to just name a few. A particularly active and interesting area is the design of new data center architectures, exploring alternatives to current (multi-rooted) fat-tree networks [2]: from alternative fat-trees [58] to fat-free [86] networks, and from trees to hypercubes [43], random graphs [88], or expanders [53]. All these designs aim to make data center networks more efficient, in terms of performance, cost, and cabling. The situation has recently been compared to the early 1980s, when the emergence of new applications led to many

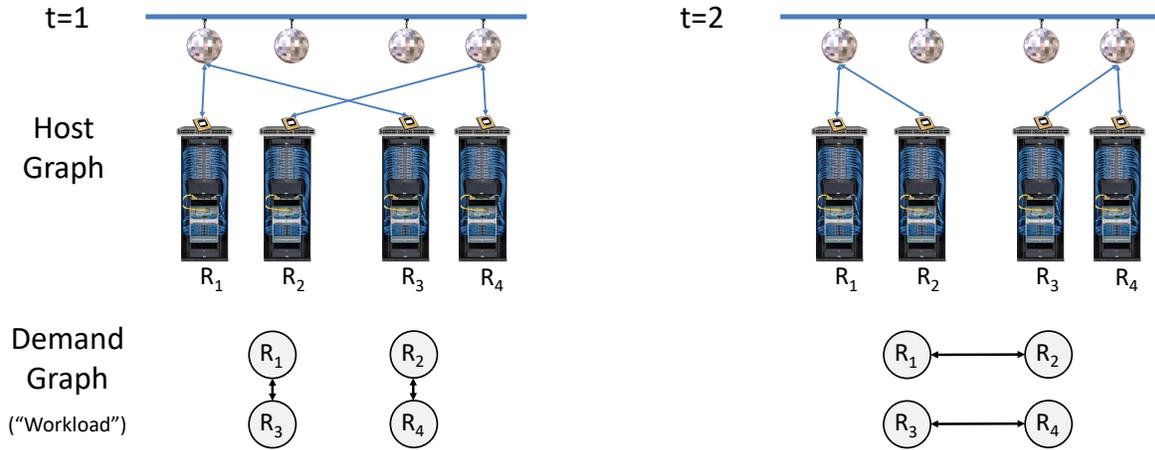


Figure 1: Illustrating example for demand-aware reconfigurable networks. Each node (here: a rack resp. top-of-rack switch)  $R_1, \dots, R_4$  can create a direct connection to another node via mirrors or “disco-balls” on the ceiling. Due to hardware constraints, only one such connection can exist for each node per epoch, i.e., the reconfigurable links form a matching. On the left side, at time  $t = 1$ , there are demands between the nodes  $R_1 \leftrightarrow R_3$  and  $R_2 \leftrightarrow R_4$ , respectively. As such, we create direct connections between them, as seen in the physical network above it. When the demand graph changes at time  $t = 2$ , the network can adapt to the new workload, by creating direct connections between  $R_1 \leftrightarrow R_2$  and  $R_3 \leftrightarrow R_4$ , respectively. However, when the demand graph no longer forms a matching, or when physical constraints prevent some reconfigurable links, the situation becomes more complex.

proposals for new interconnection network designs for parallel computers [1]. However, all these architectures also have in common that their designs are demand-oblivious and fixed.

Recent technological innovations introduce a radically new dimension to the data center network optimization problem: the possibility to reconfigure the data center topology at runtime. This technology hence has the potential to introduce a paradigm shift: it enables a vision of demand-aware data center networks which (self-)adjust to their workload (i.e., communication pattern) [12].

While the technology of such reconfigurable networks is evolving at a fast pace, these networks lack theoretical foundations: models, metrics, and algorithms. We have fallen behind the curve.

The objective of this paper is to help bridge this gap, by raising awareness in the theory community about a rich and potentially impactful research area. We first briefly discuss technological enablers and report on motivating empirical studies. Our main focus in this paper then is on the new models and algorithmic challenges introduced by this field. In particular, we will review existing algorithms and complexity results, and highlight future research directions.

The remainder of this article is structured as follows: We first give an overview of the technological enablers in §2.1, along with the empirical motivation for reconfigurable data centers in §2.2, followed by a formal model overview in §3. We then survey different algorithmic approaches for reconfigurable data center topologies in §4, also pointing out opportunities and underlying complexities. A selected (algorithmic) timeline is shown in Table 1. Lastly, we conclude in §5.

TABLE 1 Selected timeline of reconfigurable data centers

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2009	<ul style="list-style-type: none"> <li>– <i>Flyways</i> [51]: Steerable antennas (narrow beamwidth at 60 GHz [78]) to serve hotspots</li> </ul>
2010	<ul style="list-style-type: none"> <li>– <i>Helios</i> [33]/<i>c-Through</i> [98, 99]: Hybrid switch architecture, maximum matching (Edmond’s algorithm [30]), single-hop reconfigurable connections (<math>O(10)ms</math> reconfiguration time).</li> <li>– <i>Proteus</i> [21, 89]: <math>k</math> reconfigurable connections per ToR, multi-hop path stitching, multi-hop reconfigurable connections (weighted <math>b</math>-matching [69], edge-exchanges for connectivity [72], wavelength assignment via edge-coloring [67] on multigraphs)</li> </ul>
2011	<ul style="list-style-type: none"> <li>– Extension of <i>Flyways</i> [51] to better handle practical concerns such as stability and interference for 60GHz links, along with greedy heuristics for dynamic link placement [45]</li> </ul>
2012	<ul style="list-style-type: none"> <li>– <i>Mirror Mirror on the ceiling</i> [106]: 3D-beamforming (60 Ghz wireless), signals bounce off the ceiling</li> </ul>
2013	<ul style="list-style-type: none"> <li>– <i>Mordia</i> [31, 32, 77]: Traffic matrix scheduling, matrix decomposition (Birkhoff-von-Neumann (BvN) [18, 97]), fiber ring structure with wavelengths (<math>O(10)\mu s</math> reconfiguration time)</li> <li>– <i>SplayNets</i> [6, 76, 82]: Fine-grained and online reconfigurations in the spirit of self-adjusting datastructures (all links are reconfigurable), aiming to strike a balance between short route lengths and reconfiguration costs</li> </ul>
2014	<ul style="list-style-type: none"> <li>– <i>REACToR</i> [56]: Buffer burst of packets at end-hosts until circuit provisioned, employs [77]</li> <li>– <i>Firefly</i> [14] Combination of Free Space Optics and Galvo/switchable mirrors (small fan-out)</li> </ul>
2015	<ul style="list-style-type: none"> <li>– <i>Solstice</i> [57]: Greedy perfect matching based hybrid scheduling heuristic that outperforms BvN [77]</li> <li>– Designs for optical switches with a reconfiguration latency of <math>O(10)ns</math> [3]</li> </ul>
2016	<ul style="list-style-type: none"> <li>– <i>ProjecToR</i> [39]: Distributed Free Space Optics with digital micromirrors (high fan-out) [38] (Stable Matching [26]), goal of (starvation-free) low latency</li> <li>– <i>Eclipse</i> [95, 96]: <math>(1 - 1/e^{(1-\epsilon)})</math>-approximation for throughput in traffic matrix scheduling (single-hop reconfigurable connections, hybrid switch architecture), outperforms heuristics in [57]</li> </ul>
2017	<ul style="list-style-type: none"> <li>– <i>DAN</i> [7, 8, 11, 12]: Demand-aware networks based on reconfigurable links only and optimized for a demand snapshot, to minimized average route length and/or minimize load</li> <li>– <i>MegaSwitch</i> [23]: Non-blocking circuits over multiple fiber rings (stacking rings in [77] doesn’t suffice)</li> <li>– <i>Rotornet</i> [63]: Oblivious cyclical reconfiguration w. selector switches [64] (Valiant load balancing [94])</li> <li>– <i>Tale of Two Topologies</i> [105]: Convert locally between Clos [24] topology and random graphs [87, 88]</li> </ul>
2018	<ul style="list-style-type: none"> <li>– <i>DeepConf</i> [81]/<i>xWeaver</i> [102]: Machine learning approaches for topology reconfiguration</li> </ul>
2019	<ul style="list-style-type: none"> <li>– Complexity classifications for weighted average path lengths in reconfigurable topologies [34, 35, 36]</li> <li>– <i>ReNet</i> [13] and <i>Push-Down-Trees</i> [9] providing statically and dynamically optimal reconfigurations</li> <li>– <i>DisSplayNets</i> [75]: fully decentralized <i>SplayNets</i></li> <li>– <i>Opera</i> [60]: Maintaining expander-based topologies under (oblivious) reconfiguration</li> </ul>

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## 2 Technological Enablers and Empirical Motivation

Reconfigurable data center networks are enabled by emerging technologies and motivated by the rich structure often observed in communication patterns. In the following, we briefly review these technological enablers and the empirical motivations.

## 2.1 Technological Enablers

Traditionally, data center networks are based on electrical elements<sup>1</sup>, and optimized toward static properties such as scalability, construction cost, capacity, cabling complexity, latency, and/or robustness [103]. The introduction of *optical circuit switching* introduces an opportunity to save cost and improve performance through *reconfigurations*. To this end, it is important that a reconfigurable network is (1) *agile*, i.e., provides low *reconfiguration time* and (2) supports a high *fan-out*, a large number of different nodes can be connected at any given time as well as over time. Most emerging reconfigurable data center topologies leveraging optical circuit switching are *hybrid* in the sense that they combine optical circuit switching with conventional electrical packet switching. As we discuss later, reconfigurable topologies can also be implemented by wireless technologies and free-space optics, but also via electric solutions [5, 20]. For example, optical circuit switching is used to dynamically interconnect only (a subset of) top-of-rack switches and reserved for elephant flows. An additional conventional topology is commonly used for the remaining traffic (e.g., mice flows). Mice flows can also be routed along the reconfigurable topology, e.g., under extremely fast reconfiguration times [32] or when the reconfigurable topology always forms a connected multi-hop network [21], simulating a static topology by priority queuing [60].<sup>2</sup>

Different reconfigurable networks differ by the type of reconfigurable devices used. We refer to [19, 39, 46, 65, 92, 103] for a further overview, as detailing all technologies, such as e.g. thermo-optic switches [54], goes far beyond the scope of this paper. The three most common technologies allowing to dynamically change the capacity between pairs of ToRs<sup>3</sup> are:

1. **Optical Circuit Switches (OCS):** Examples employing OCSes include *Helios* [33], *RE-ACToR* [56], *Solstice* [57], *Mordia* [77], *OSA* [21], *c-Through* [99], among others. To provide some intuition, an OCS can be understood as “*a Layer 0 switch — it operates directly on light beams without decoding any packets*” [33]. There are multiple technological enablers in this setting. Micro-Electro-Mechanical System (MEMS) switches employ small mirrors controlled by motors, typically reconfiguring in  $O(10)ms$  in a  $N \times N$  crossbar 3D-MEMS setting. While these mirrors can be redesigned to react in smaller timescales, there is a tradeoff regarding the number of ports, see [62] for a discussion on the scaling limits. A Wavelength-Selective Switch (WSS) is usually a  $1 \times N$  switch that distributes the incoming wavelengths over the  $N$  output ports, with an extra bypass port for the remaining wavelengths. Such WSSes can be built with faster 2D-MEMS, at the cost of having smaller port counts. A related enabler is Wavelength Division Multiplexing (WDM), where typically 40 or more wavelengths can be used on a single fiber [21]. For example the *Mordia* [77] prototype achieves a  $O(10)\mu s$  reconfiguration time with 24 ports. However, such switches can also be combined and extended, see e.g. [23, 3.1] for a discussion on how to reach  $O(100k)$  ports. Another idea to increase the port count while remaining at a  $O(10)\mu s$  reconfiguration time is to reduce the number of different combinations the switch can serve [64]. WSSes can also be realized without 2D-MEMS, see e.g. *WaveCube* [22] with a reconfiguration time of  $\sim 10ms$ . Lastly, technology with a reconfiguration time in the order of nanoseconds is also emerging, e.g. [3], we refer to the various references in [92] for further technology proposals and details.

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<sup>1</sup> However, “*optical fibers are gaining the momentum with high data rates, low transmission loss, and low power consumption*” [103]. Such optical fibers do not necessarily imply a reconfigurable network, they can also be used as fixed links. <sup>2</sup> Notwithstanding, “*Supporting low-latency traffic without a hybrid network is a subject of ongoing investigation*” [65]. <sup>3</sup> In one of the earlier papers, extra links between pairs of ToRs were called *flyways* [51].

2. **60 GHz Wireless:** 60 GHz technology can support short range (1-10 meters), high-bandwidth wireless links in an unlicensed band. Examples include *Flyways* [45, 51] and the 3D-beamforming approach by Zhou et al. [106]. In order to extend the “line-of-sight” and reduce interference, mirrors can be used, e.g., on the ceiling [106]. Regarding the reconfiguration time, *Flyways* [45, §3.1] mentions that antennas can be steered in  $O(100)\mu s$ , but ignores such overheads in their simulations. Ranges from 0.01 to 1 second are reported in [106, §4.1]. In order to overcome mechanical steering delays, antenna arrays can be used, trading in a lower fan-out and greater interference for smaller delays in the range of  $O(10)ns$ , see [106, §6] for a discussion.
3. **Free-Space Optics (FSO):** FSO is another emerging technology which usually relies on *lasers* to enable high-capacity communication between nodes (e.g., top-of-rack switches) through the “free space”. In general, FSO is expected to offer lower interference and higher bandwidth over long ranges than 60 GHz communication. Similar to wireless links, it is e.g. known that the mechanical steering of FSO links (e.g., using vertical and rotational motion) [79] or the use of Galvo/switchable mirrors [14] results in relatively high switching times (e.g.  $\sim 20ms$  in *FireFly* [14]) compared to the Digital Micromirror Devices (DMD) used e.g., in *ProjecToR* [39] ( $7 - 12\mu s$ ); DMD-based solutions such as *ProjecToR* also increase the fan-out and can *scale* better than e.g. MEMS-based solutions [61].

## 2.2 Empirical Motivation

There exist several empirical studies demonstrating the potential of reconfigurable networks. For the following discussion, we classify reconfigurable networks along the following dimension:

- **Demand-Oblivious Networks:** Demand-oblivious reconfigurable networks such as *Rotornet* [63] and *Opera* [60] change the topology according to a *fixed* schedule which is *independent* of the workload.
- **Demand-Aware Networks:** The topology of demand-aware networks is adjusted to optimally serve the current traffic pattern.

A key advantage of demand-oblivious reconfigurable networks is their simplicity and high-degree of decentralization: empirical studies show that solutions like *Rotornet* [63] and *Opera* [60] can provide high capacity (emulating a “complete graph”) without sacrificing scalability, at the price of a small latency cost.

Demand-aware networks, in contrast, can also leverage *structure in the demand* (i.e., traffic pattern or workload): the usefulness of demand-aware and self-adjusting networks hence depends on the amount of exploitable structure there is in the demand.<sup>4</sup>

Existing measurement studies show that real-world communication patterns are indeed often far from all-to-all: depending on the application mix in the data center [80], traffic patterns feature spatial and temporal structure, i.e., are sparse and only a small fraction of all possible source-destination pairs are involved in intensive communications at any time [80]. For example, empirical studies in data centers found that in a given time interval, a high percentage of rack pairs does not exchange any traffic at all, and that less than 1% of the rack pairs account for 80% of the total

<sup>4</sup> We note that the problem of actually obtaining the exact current demands is not trivial, unless they are e.g. provided by a central scheduling system. Notwithstanding, the situation is not as grim as it may seem, as even “*a presumed lack of advance knowledge of flow sizes is not necessarily prohibitive for highly efficient scheduling*” [28]. We refer to e.g. [77, §3.4] for an introductory overview of demand estimation.

traffic [39]. Data center traffic further exhibits regionality and some stability [21]: only a few ToRs are hot and most of their traffic goes to a few other ToRs [51, 52], over 90% bytes flow in elephant flows [42], traffic at ToRs exhibits an ON/OFF pattern [16], 60% ToRs see less than 20% change in traffic volume for between 1.6-2.2 seconds [17], and a production DCN traffic shows stability even on a hourly time scale [108]. Such empirical studies hence indicate that demand-aware reconfigurable networking technology may lower costs without affecting performance [14].

### 3 Models and Taxonomy

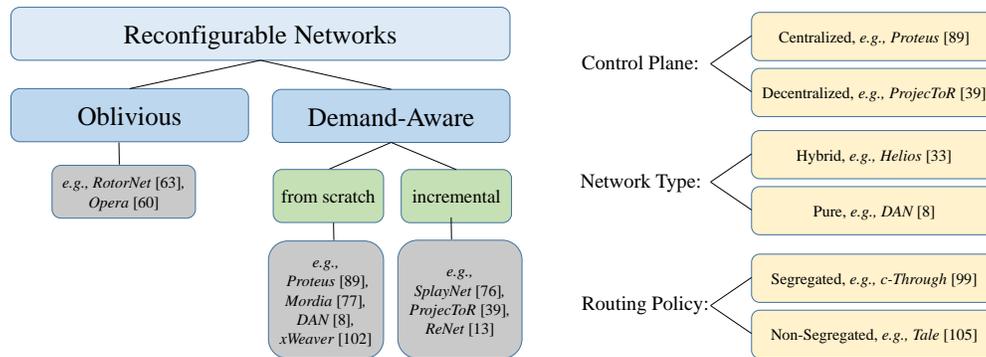


Figure 2: Taxonomy overview for reconfigurable data centers, with a selected example for each category. On the left side, we classify the different types of reconfigurable networks and give examples. On the right side, we list additional dimensions along which existing models differ: related to the control plane, whether the physical network is hybrid or not, related to routing, etc. We omit the dimension of different objective functions for clarity.

Essentially, reconfigurable data center networks pose the following graph theoretic problem: how to augment the topology with additional edges, s.t. some desired objective function is optimized? However, unlike in classic graph augmentation [25, 66, 73, 74], these edges cannot be added<sup>5</sup> arbitrarily. Rather, the edge additions are subject to real-world constraints such as degree (e.g. a node only has one physical receiver) or connectivity (e.g. distance or objects blocking the signal path). In some shared mediums, coloring algorithms for different wavelengths also come into play.

**It’s a Match(ing)!** In the most basic case, the network nodes are connected to a reconfigurable (e.g. optical circuit) switch, which creates direct physical connections between the nodes. However, these connections can only be formed between two nodes each time, i.e., more formally, the output is a matching (Fig. 1). In turn, matching links are added to the topology, augmenting the network to better serve the demands. While such an augmentation should take the fixed static topology into account, there are also various proposals that completely omit non-reconfigurable links.

Moreover, reconfigurable networks can offer great flexibility beyond a single reconfigurable switch. 1) Multiple such switches can be added, also interconnecting them, where each switch might only connect a subset of nodes, 2) nodes can also be connected multiple times to a single switch, allowing for multi-matchings, 3) reconfigurable links can be uni- or bidirectional, depending on technology and policy, and 4) the reconfigurable switches might only allow a subset of all possi-

<sup>5</sup> In some settings, it can also be interesting to study the *removal* of edges, see e.g. [107, §5.1].

ble matchings, to increase the reconfiguration time. Nonetheless, the fundamental problem setting remains unchanged: Given a static graph e.g.,  $G = (V, E)$ , how to augment this graph with some matchings (subject to various constraints) s.t. some objective function is optimized?

Lastly, there is a multitude of technologies that implements the above settings, most notably optical circuit switching, wireless (beamforming), and free-space optics, but also shared mediums such as fiber ring structures and completely electric solutions. In some settings, e.g., fiber rings and wireless, it is also possible to dynamically create one-to-many connections. If the transmission medium is shared, additional constraints can come into play, such as interference between signals (channels for non-beamforming wireless) and different colors for different wavelengths on fiber.

**Routing Part I: From Snapshots to Ongoing Reconfiguration.** Routing the data itself adds another layer of complexity to reconfigurable networks, as the control plane has to distribute the new routing paths that are enabled by the topology reconfiguration. An orthogonal approach is to periodically schedule the topology changes ahead of time, s.t. a calendar is available to all nodes (i.e., being demand-oblivious). In such a setting, it can also be advantageous if some traffic can be buffered, in order to send it when a fitting topology appears.

To be more demand-aware, a first idea is to optimize the networks for snapshots, i.e., the network topology is reconfigured only once for some demands or communication pattern. Such a reconfiguration can then be scheduled at periodic intervals, to adapt the network to current settings. Improved hardware for faster reconfiguration motivated the idea of changing the topology multiple times for a single snapshot, coined traffic matrix scheduling. Under traffic matrix scheduling, the logically centralized control plane can bundle the upcoming changes, instead of multiple interactions. Moreover, e.g. the throughput can be improved over a single reconfiguration.

For better scaling and faster response times, the control plane can also be distributed, where the nodes run some distributed protocol to arrange for the routing (and ideally the topology reconfiguration as well). Beyond snapshots, one can also consider request-based models, where the topology is adapted online for each incoming (transfer) request.

**Routing Part II: Routing Policies.** Whereas multi-hop routing is standard in most computer networks, the situation is not so clear in reconfigurable networks. Enforcing that reconfigurable connections may only be used along a single hop has the advantage that there are no dependencies on other links, i.e., links can be reconfigured independently, as long as the matching is valid. Along the same lines, policies restricted to single-hop connections are easier to analyze and optimize. Notwithstanding, enforcing single-hop connections is usually an artificial constraint, which degrades the maximum theoretical system performance by limiting the routing options. On the other hand, the system performance also suffers if topology and routing changes take unreasonably long to compute and distribute.

**Optimization Objectives.** Ideally, a networked system should be optimized for a multitude of objectives, in particular throughput maximization (each edge has some capacity), low latency respectively weighted average path lengths (edges have weights), and optimized flow completion times, as well as starvation and fairness issues. In practice, most approaches primarily optimize their algorithms for a single goal, implicitly optimizing the other objectives as well.

## 4 Algorithmic Opportunities and Complexity

We now investigate different classes of algorithms used to reconfigure data center networks in various approaches. Besides (intractable) mixed integer programs and (greedy) heuristics, we can

roughly categorize most proposed algorithms into those motivated by matchings (§4.1) and those inspired by datastructures and coding (§4.2). Further approaches are briefly discussed in §4.3.

## 4.1 Matching Algorithms

As reconfiguring network topologies, at its core, is a matching problem (except for contention in shared mediums), it is not surprising that a variety of matching algorithms are studied in the literature.

**Maximum Matching Algorithms.** The motivation behind maximum matching algorithms, as employed by, e.g., *Helios* [33] and *c-Through* [99], is twofold. First, matching algorithms are usually fast, e.g. Edmond’s algorithm [30]. Second, if the network may only be reconfigured once for a snapshot and the only objective is to maximize single-hop throughput along reconfigurable links, then the throughput is optimal: every possible link can be assigned a benefit, where Edmond’s algorithm [30] optimizes the sum of benefits. However, when accounting for the static topology as well, the performance deteriorates, and such settings are mostly unstudied from a theoretical perspective. Multiple reconfigurations for a single snapshot are discussed in the next paragraph.

*Proteus* [89] and its extension *OSA* [21] employ an analogous idea for multiple reconfigurable connections per node by employing weighted  $b$ -matching [69] algorithms.<sup>6</sup> In order to create a connected graph for multi-hop routing, *OSA* [21] then leverages edge-exchange [72] operations. Notwithstanding, no theoretical guarantees for the performance of multi-hop routing are provided.

For minimizing the average weighted path length, maximum ( $b$ -)matching algorithms can be directly adapted to yield optimal results, for the case of segregated routing policies (single-hop reconfigurable xor multi-hop static) [35, 36]. Non-segregated routing policies (joint routing on reconfigurable and static parts, along multiple hops) turn the problem NP-hard again [35, 36], only heuristics without approximation guarantees are known so far [14, §5.1], [34].

**Traffic Matrix Scheduling.** Motivated by faster hardware reconfiguration times, *Mordia* [77] proposed to reconfigure the network multiple times for a single (traffic demand) snapshot. To this end, the traffic demand matrix is scaled into a bandwidth allocation matrix, which represents the fraction of bandwidth every possible matching edge should be allocated in an ideal schedule. Next, the allocation matrix is decomposed into a schedule, employing a computationally efficient [41] Birkhoff-von-Neumann decomposition, resulting in  $O(n^2)$  reconfigurations and durations. However, *Mordia* [77] did not account for the static part of the network (hybrid architectures) and also did not minimize the number of reconfigurations, each still inducing some delay.

*Solstice* [57] takes hybrid architectures and time constraints into account (which turn the problem NP-hard [55]) but does not provide theoretical guarantees for their greedy scheduling heuristic. A further literature overview for different reconfiguration delays is found in [57, §6] as well.

*Eclipse* [96] can provide an  $(1 - 1/e^{(1-\epsilon)})$ -approximation for throughput in the hybrid switch architecture with reconfiguration delay, but only for direct routing along single-hop reconfigurable connections. The indirect routing (multi-hop) case remains open w.r.t. provable guarantees.

**Stable Matching Algorithms.** A significant downside of the previously in this section discussed algorithms is that they rely on centralized computation and coordination. Stable matching algorithms have the benefit that it is easier to implement them in a distributed fashion, also allowing for solutions that are incremental, i.e., to keep the current topology state in mind. *ProjecToR* [39] employs (Gale-Shapley [37]) stable matching algorithms with the goal of minimizing (the  $\ell_2$  norm

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<sup>6</sup> Regarding the quality of such solutions, please see the paragraph on stable matching algorithms below.

of) latencies, accounting for starvation via aging of requests. In their setting, reconfigurable connections are single-hop, with multiple senders and receivers for a subset of nodes, and control packets are exchanged over the static topology. Even though their algorithm performs online computations, they can achieve a constant-factor approximation for latencies.

Instantaneous throughput optimization in this setting would be computationally easy (under centralized computation), if each node can match to each other node at most once. When optimizing for path lengths, we can assume this constraint, as parallel edges do not decrease distances. For throughput however, the situation is unclear, and it is not known if the problem is NP-hard [26].

**Oblivious Matching Schedules.** An orthogonal approach to centralized versus distributed reconfiguration is to be completely oblivious (cf also Figure 2) to current demands and pre-compute a *fixed* periodic scheduling for topology reconfigurations. One idea could be to cycle through all possible sets of matchings, which however would take an unreasonably long time. Instead, *Rotornet* [63] proposes to just use a small set of matchings, s.t. connectivity between endpoints is guaranteed in a matching cycle. In this setting, the reconfiguration time of switches can also be improved, as the switches only need to implement a small subset of all possible matchings. In case of uniform (delay-tolerant) traffic, such single-hop forwarding can saturate the network’s bisection bandwidth [63]. Still, for skewed traffic matrices, many direct connections will remain underutilized. To this end, *Rotornet* [63] uses Valiant load balancing [94] and multiple reconfigurable switches to perform distributed multi-hop routing, coupled with buffering of such indirect traffic.

In the context of purely static topologies, building the network topology itself can also be understood as an oblivious reconfiguration—just only once. As common networking technology has a small number of ports (often identical throughout the data center), *Jellyfish* [88] proposes to use random regular graphs to obtain good throughput, leveraging low average path length and the ability for route traffic through underutilized network parts for skewed traffic matrices. The deterministic version of this idea relies on  $d$ -regular expander graphs, which are only a logarithmic throughput factor away from  $d$ -regular graphs built for specific traffic matrices [93]. A proposal how to realize such expanders as a fixed data center topology was presented in [53].

These insights for random and expander graphs also found their way into reconfigurable data centers shortly after. In a *Tale of Two Topologies* [105], the topology is reconfigured to locally convert between Clos and random graphs, though in a demand-aware fashion. Moreover, *Opera* [60] extends the ideas of *Rotornet* [63] by maintain expander graphs in its periodic reconfigurations. Even though the reconfiguration scheduling of *Opera* is deterministic and oblivious, the precomputation of the topology layouts is in its current form still randomized.

## 4.2 Datastructure and Coding Approaches

Another approach to design demand-aware reconfigurable networks leverages an interesting connection to datastructures and coding. Existing datastructure- and coding-based approaches fall into two categories (according to our more general taxonomy in Figure 2):

- **Fixed demand-aware networks:** These networks (e.g., [7, 8, 11, 12]) are optimized toward a given snapshot of the demand. The objective is usually to minimize the expected path length [7, 8, 11, 12] but there also examples additionally minimizing the network load [11] or resilience [7].
- **Self-adjusting demand-aware networks:** These networks (e.g., [6, 12, 75, 76, 82]) adjust in a fine-grained manner, trying to react *quickly and locally* two new communication requests.

The objective is to strike an optimal tradeoff between the benefits of reconfigurations (e.g., shorter routes) and their costs (e.g., reconfiguration latency, energy, packet reorderings, etc.)

The main observation underlying these approaches is that designing an “optimal” reconfigurable network for a *single* source is related to the design of Binary Search Trees (BSTs) or Huffman coding. We briefly elaborate on this connection by discussing the fixed and the self-adjusting problems in turn:

- *Fixed*: If the distribution of which keys are accessed more frequently in a BST is known, it is possible to optimize the BST towards its demand: i.e., to compute a *biased* BST. Similarly, if the frequency distribution of which letters need to be communicated is known, it is possible to optimize the encoding of those letter, e.g., using a Huffman tree: the expected number of to-be-communicated bits is reduced.
- *Self-adjusting*: Even if the demand is not known ahead of time, it is possible to adjust a binary search tree or an encoding *in an online manner*, e.g., using *splay trees* (in case of BSTs) or *dynamic Huffman trees* (in case of coding).

The idea is then to organize the communication partners (i.e., the destinations) of a *given* communication source in either a static binary search or Huffman tree (if the demand is known), or in a dynamic tree (if the demand is not known or if the distribution changes over time). The former approach is used in *DANs* [8, 11, 12], the latter approach is used in *SplayNets* [6, 12, 75, 76, 82], in *Push-Down-Trees* [10], and in *ReNets* [13]. The tree optimized for a single source is sometimes called the *ego-tree* [11], and the approach relies on combining these ego-trees of the different sources into a network, while keeping the resulting node degree constant and preserving distances (i.e., low distortion). One exception to this general approach is *rDAN* [7], which relies on Shannon-Fano-Elias coding and a continuous-discrete approach [70].

Besides algorithms to design demand-aware reconfigurable networks, the connection to data-structures and coding also provides *metrics* for demand-aware networks. Recall that in demand-oblivious BSTs, the average access cost is  $O(\log n)$ , where  $n$  is the size of the BST; similarly, worst-case coding requires  $O(\log n)$  bits per transmitted letter. In contrast, the expected lookup cost in *biased* BSTs or the expected number of bits per symbol in a Huffman tree, is proportional to the *entropy* of the demand, which can be much lower. Similarly, it has recently been shown that (a variant of) the entropy of the demand is also a useful metric for the performance achievable by a reconfigurable network.

### 4.3 Further Approaches

The observation that traffic demands feature much structure naturally leads to the question whether this structure could also be exploited using machine learning. To just give two examples, *xWeaver* [102] and *DeepConf* [81] use neural networks to provide traffic-driven topology adaptation. Another approach is taken by Kalmbach et al. [50], who aim to strike a balance between topology optimization and “keeping flexibilities”, leveraging the concept of *empowerment* (e.g., known from robotics).

In the context of shared mediums (e.g., non-beamformed wireless broadcast, fiber<sup>7</sup> (rings)), contention and interference of signals can be avoided by using different channels and wavelengths. The algorithmic challenge is then to find (optimal) edge-colorings on multi-graphs, an NP-hard

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<sup>7</sup> In the context of data center proposals, shared fiber is the more common medium, e.g., in [21, 23, 77].

problem for which fast heuristics exist [67]. However, on specialized topologies optimal solutions can be found in polynomial time, e.g., in *WaveCube* [22]. Shared mediums also have the benefit that it is easier to distribute data in a one-to-many setting [100]. For example on fiber rings, all nodes on the ring can intercept the signal [23, §3.1]. One-to-many paradigms<sup>8</sup> such as multicast can also be implemented in other technologies, using e.g., optical splitters for optical circuit switches or half-reflection mirrors for free-space optics [15, 90, 91, 104]. The challenges posed by one-to-one transfers carry over to this setting, in particular results with provable (approximation) guarantees on reconfigurable multi-hop routing would be of interest.

## 5 Conclusion and Future Work

We presented a survey of emerging reconfigurable data centers, discussing technological enablers and empirical studies in the literature, and with an emphasis on the algorithmic problems such data centers introduce. While we deliberately omitted many details, especially on the technological enablers of demand-aware networks, we hope that our paper can serve the community as a motivation to dig deeper in the referenced literature, and contribute toward more refined models and improved algorithmic techniques, accordingly. Finally, we also note that reconfigurable networks are not only arising in data centers, but for example also in the wide-area [29, 44, 48, 49, 59, 84, 85], introducing further interesting avenues for research.

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<sup>8</sup> Conceptually similar challenges also appear in the context of coflows [47, 101].

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